

PSFP-24-3832M-22F

DATASHEET

FEATURES:

- Multi-Source Package with Duplex LC Connector
- Up to 1.25Gb/s Data Links
- Single +3.3V Power Supply
- Hot-Pluggable
- Compliant with Bellcore TA-NWT-000983
- Compliant with Specifications for IEEE802.3Z
- Eye Safety Designed to Meet Laser Class1, Compliant with IEC60825
- Monitoring Interface Compliant with SFF-8472, Real time monitoring of:
 - Transmitter optical power
 - o Receiver optical power
 - Laser bias current
 - o Temperature
 - Supply voltage
- RoHS Compliant Products

APPLICATIONS:

- Gigabit Ethernet
- 1x Fiber Channel
- Switch to Switch Interface
- Router/Server Interface
- Other Optical Links

SPECIFICATIONS:

Electrical Characteristics ($T_{OP} = T_{c_r} V_{CC} = 3.0 \text{ to } 3.60 \text{ Volts}$)

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Transmitter:						
Input Differential Impedance	R _{in}	90	100	110		
Single Ended Data Input Swing	V _{in PP}	250		1200	mVp-p	
Transmit Disable Voltage	V _D	V _{cc} – 1.3		V _{cc}	V	2
Transmit Enable Voltage						
Transmit Disable Assert Time						
Receiver:						
Single Ended Data Output Swing	$V_{\text{out,pp}}$	250		800	Mv	3
LOS Fault	V _{losfault}	V _{cc} – 0.5		V _{cc_host}	V	5
LOS Normal	V _{los norm}	Vee		V _{ee} +0.5	V	5
Power Supply Rejection	PSE	100			mV _{pp}	6



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Note:

- 1. AC coupled.
- 2. Or open circuit.
- 3. Into 100 ohm differential termination.
- 4. 20 80 %
- 5. LOS is LVTTL. Logic 0 indicates normal operation; logic 1 indicates no signal detected.
- 6. All transceiver specifications are compliant with a power supply sinusoidal modulation of 20 Hz to 1.5MHz up to specified value applied through the power supply filtering network shown on page 23 of the Small Form-factor Pluggable (SFP) Transceiver Multi-Source Agreement (MSA), September 14, 2000.
- 7. Measured with DJ-free data input signal. In actual application, output DJ will be the sum of input DJ and. DJ.

Optical Parameters (Top = Tc, Vcc = 3.135 to 3.465 Volts)

Transmitter:

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Centre Wavelength	λ_{c}	840	850	860	nm	
Output Spectral Width	O RMS		-	0.85	nm	
Average Output Power	P _{out}	-9		-3	dBm	1
Extinction Ratio	ER	9			dB	
Optical Rise/Fall Time	_{tr} / t _f		-	260	ps	2
Relative Intensity Noise	RIN			-120	dB/Hz	
Output Optical Eye						

Receiver:

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Optical Input Wavelength	λ_{c}	770		860	nm	
Receiver Overload	Pol	0			dBm	3
RX Sensivity	Sen			-17	dBm	3
RX_LOS Assert	LOS A	-35			dBm	
RX_LOS De-Assert	LOS _D			-18	dBm	
RX_LOS Hysteresis	LOS _H	0.5			dB	



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General Specifications:

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Data Rate	BR		1.25		Gb/s	
Bit Error Rate	BER			10-12		
Max. Supported Link (50/125um)	L _{MAX}		550		m	
Total System Budget	LB	8			dB	

Note

- 1. The optical power is launched into MMF.
- 2. 20-80%.
- 3. Measured with PRBS 27-1 at 10-12 BER

Absolute Maximum Ratings:

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	T _{ST}	-40	+85	℃
Operating Temperature	T_{IP}	-45	+85	℃
Input Voltage	Tcc	-0,5	+4	V

Recommended Operating Environment:

Parameter	Symbol	Min.	Typical	Max.	Unit
Supply Voltage	Vcc	+3.135	+3.3	+3.465	V
Supply Current	\mathbf{I}_cc			300	mA
Inrush Current	${ m I}_{\sf surge}$			I _{cc} +30	mA
Maximum Power	P _{max}			1	W
Operating Temperature	Top	-40	-	+85	°C

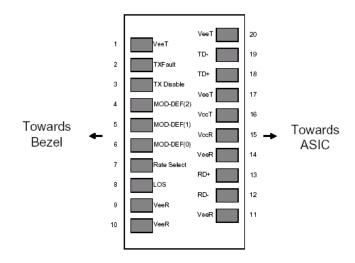
Digital Diagnostic Monitor Characteristics:

Parameter	Symbol	Min.	Max.	Unit
Temperature Monitor Absolute Error	DMI_Temp	-3	3	degC
Laser Power Monitor Absolute Error	DMI_TX	-3	3	dB
RX Power Monitor Absolute Error	DMI_RX	-3	3	dB
Supply Voltage Monitor Absolute Error	DMI_VCC	-0.1	0.1	V
Bias Current Monitor Absolute Error	DMI_Ibias	-10%	10%	mA



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Pin Assignment:



Pin out of Connector Block on Host Board

Pin Description:

Pin	Symbol	Name/Description	Ref.			
1	V_{EET}	Transmitter Ground (Common with Receiver Ground)				
2	T _{FAULT}	Transmitter Fault. Low normal operation, High Fault indication				
3	T _{DIS}	Transmitter Disable. Laser output disabled on high or open.	2			
4	MOD_DEF(2)	Module Definition 2. Data line for Serial ID.	3			
5	MOD DEF(1)	Module Definition 1. Clock line for Serial ID.	3			
6	MOD_DEF(0)	Module Definition 0. Grounded within the module.	3			
7	Rate Select	No connection required				
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation.	4			
9	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1			
10	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1			
11	V _{EER}	Receiver Ground (Common with Transmitter Ground)	1			
12	RD-	Receiver Inverted DATA out. AC Coupled	Ĭ.			
13	RD+	Receiver Non-inverted DATA out. AC Coupled				
14	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1			
15	V _{CCR}	Receiver Power Supply				
16	V _{cct}	Transmitter Power Supply	1			
17	V_{EET}	Transmitter Ground (Common with Receiver Ground)	1			
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.				
19	TD-	Transmitter Inverted DATA in. AC Coupled.				
20	V_{EET}	Transmitter Ground (Common with Receiver Ground)	1			

Notes:

- $1. \ \, \hbox{Circuit ground is internally isolated from chassis ground.}$
- 2. Laser output disabled on TDIS >2.0V or open, enabled on TDIS <0.8V.
- Should be pulled up with 4.7k 10kohms on host board to a voltage between 2.0V and 3.6V. MOD_DEF(0) pulls line low to indicate module is plugged in.
- 4. LOS is open collector output. Should be pulled up with 4.7k 10kohms on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.

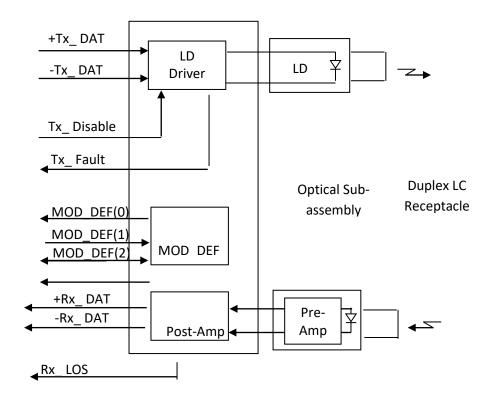


1.25Gb/s Hot Pluggable, Duplex LC,

+3.3V, 850nm, VCSEL, Multi-Mode SFP Optical Transceiver with DDMI, Industrial Temperature Rated

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Block Diagram of Transceiver:





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Serial ID Memory Contents:

Data	Length	Name of	5 10		
Address	(Byte)	Length	Description and Contents		
Base ID Fields	3				
0	1	Identifier	Type of Serial transceiver (03h=SFP)		
1	1	Reserved	Extended identifier of type serial transceiver (04h)		
2	1	Connector	Code of optical connector type (07=LC)		
3-10	8	Transceiver	Gigabit Ethernet 1000Base-SX & Fiber Channel		
11	1	Encoding	8B10B (01h)		
12	1	BR,Nominal	Nominal baud rate, unit of 100Mbps		
13-14	2	Reserved	(0000h)		
15	1	Length(9um)	Link length supported for 9/125um fiber, units of 100m		
16	1	Length(50um)	Link length supported for 50/125um fiber, units of 10m		
17	1	Length(62.5um)	Link length supported for 62.5/125um fiber, units of 10m		
18	1	Length(Copper)	Link length supported for copper, units of meters		
19	1	Reserved			
20-35	16	Vendor Name	SFP vendor name: PeakOptical®		
36	1	Reserved			
37-39	3	Vendor OUI	SFP transceiver vendor OUI ID		
40-55	16	Vendor PN	Part Number: "PSFP-xxxxxx" (ASCII)		
56-59	4	Vendor rev	Revision level for part number		
60-62	3	Reserved			
63	1	CCID	Least significant byte of sum of data in address 0-62		
Extended ID F	ields				
64-65	2	Option	Indicates which optical SFP signals are implemented		
			(001Ah = LOS, TX_FAULT, TX_DISABLE all supported)		
66	1	BR, max	Upper bit rate margin, units of %		
67	1	BR, min	Lower bit rate margin, units of %		
68-83	16	Vendor SN	Serial number (ASCII)		
84-91	8	Date code	PeakOptical®'s Manufacturing date code		
92-94	3	Reserved			
95	1	CCEX	Check code for the extended ID Fields (addresses 64 to 94)		
Vendor Specif	ic ID Fields				
96-127	32	Readable	PeakOptical® specific date, read only		



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Serial ID Memory Contents: (A2H)

Address	ess # Bytes Name		Description
00-01	2 Temp High Alarm		MSB at low address
02-03	2	Temp Low Alarm	MSB at low address
04-05	2	Temp High Warning	MSB at low address
06-07	2	Temp Low Warning	MSB at low address
08-09	2	Voltage High Alarm	MSB at low address
10-11	2	Voltage Low Alarm	MSB at low address
12-13	2	Voltage High Warning	MSB at low address
14-15	2	Voltage Low Warning	MSB at low address
16-17	2	Bias High Alarm	MSB at low address
18-19	2	Bias Low Alarm	MSB at low address
20-21	2	Bias High Warning	MSB at low address
22-23	2	Bias Low Warning	MSB at low address
24-25	2	TX Power High Alarm	MSB at low address
26-27	2	TX Power Low Alarm	MSB at low address
28-29	2	TX Power High Warning	MSB at low address
30-31	2	TX Power Low Warning	MSB at low address
32-33	2	RX Power High Alarm	MSB at low address
34-35	2	RX Power Low Alarm	MSB at low address
36-37	2	RX Power High Warning	MSB at low address
38-39	2	RX Power Low Warning	MSB at low address
40-55	16	Reserved	Reserved for future monitored quantities



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Address	# Bytes	Name	Description
56.50	4	D., DMD(4)	Single precision floating point calibration data - Rx optical power.
56-59	4	Rx_PWR(4)	Bit 7 of byte 56 is MSB. Bit 0 of byte 59 is LSB.
60.63	4	D., DMD(2)	Single precision floating point calibration data - Rx optical power.
60-63	50-63 4	Rx_PWR(3)	Bit 7 of byte 60 is MSB. Bit 0 of byte 63 is LSB.
64-67	4	Dy DWD(2)	Single precision floating point calibration data - Rx optical power.
04-07	4	Rx_PWR(2)	Bit 7 of byte 64 is MSB, bit 0 of byte 67 is LSB.
68-71	4	Rx_PWR(1)	Single precision floating point calibration data - Rx optical power.
00-71	7	KX_FWK(1)	Bit 7 of byte 68 is MSB, bit 0 of byte 71 is LSB.
72-75	4	Dy DMD(O)	Single precision floating point calibration data - Rx optical power.
72-73	4 Rx_PWR(0)	KX_FWK(0)	Bit 7 of byte 72 is MSB, bit 0 of byte 75 is LSB.
76-77	, ,	Ty I(Clana)	Fixed decimal (unsigned) calibration data, laser bias current.
/6-//	2	Tx_I(Slope)	Bit 7 of byte 76 is MSB, bit 0 of byte 77 is LSB.
			Fixed decimal (signed two's complement) calibration data,
78-79	78-79 2	2 Tx_I(Offset)	laser bias current.
			Bit 7 of byte 78 is MSB, bit 0 of byte 79 is LSB
		Tx_PWR(Slope)	Fixed decimal (unsigned) calibration data,
80-81	2		transmittercoupled output power.
			Bit 7 of byte 80 is MSB, bit 0 of byte81 is LSB.
			Fixed decimal (signed two's complement) calibration data,
82-83	2	Tx_PWR(Offset)	transmitter coupled output power.
			Bit 7 of byte 82 is MSB, bit 0 of byte 83 is LSB.
			Fixed decimal (unsigned) calibration data,
84-85	2	T(Slope)	internal module temperature.
			Bit 7 of byte 84 is MSB, bit 0 of byte 85 is LSB.
			Fixed decimal (signed two's complement) calibration data,
86-87	2	T(Offset)	internal module temperature.
			Bit 7 of byte 86 is MSB, bit 0 of byte 87 is LSB.
			Fixed decimal (unsigned) calibration data,
88-89	2	V(Slope)	internal module supply voltage.
			Bit 7 of byte 88 is MSB, bit 0 of byte 89 is LSB.
			Fixed decimal (signed two's complement) calibration data,
90-91	2	V(Offset)	internal module supply voltage.
			Bit 7 of byte 90 is MSB. Bit 0 of byte 91 is LSB.
92-95	4	Reserved	Reserved



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Byte	Bit	Name	Description
Conver	ted ar	nalog values. Calibrated 16 bit da	ta
96	All	Temperature MSB	Internally measured module temperature.
97	All	Temperature LSB	
98	All	Vcc MSB	Internally measured supply voltage in transceiver.
99	All	Vcc LSB	
100	All	TX Bias MSB	Internally measured TX Bias Current.
101	All	TX Bias LSB	
102	All	TX Power MSB	Measured TX output power.
103	All	TX Power LSB	
104	All	RX Power MSB	Measured RX input power.
105	All	RX Power LSB	
106	All	Reserved MSB	Reserved for 1st future definition of digitized analog input
107	All	Reserved LSB	Reserved for 1st future definition of digitized analog input
108	All	Reserved MSB	Reserved for 2nd future definition of digitized analog input
109	All	Reserved LSB	Reserved for 2nd future definition of digitized analog input
Option	al Sta	tus/Control Bits	
110	7	TX Disable State	Digital state of the TX Disable Input Pin. Not supported.
110	6	Coft TV Disphle	Read/write bit that allows software disable of laser.
110	ь	Soft TX Disable	Not supported.
110	5	Reserved	
110	4	RX Rate Select State	Digital state of the SFP RX Rate Select Input Pin.
110	4	KA Rate Select State	Not supported.
110	3	Soft RX Rate Select	Read/write bit that allows software RX rate select.
			Not supported.
110	2	TX Fault	Digital state of the TX Fault Output Pin.
110	1	LOS	Digital state of the LOS Output Pin.
110	0	Data Ready	Indicates transceiver has achieved power up and data is ready
111	7-0	Reserved	Reserved.



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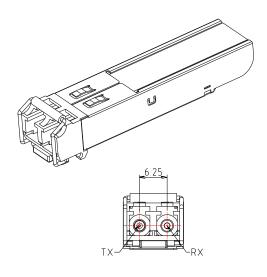
Byte	Bit	Name	Description			
Reserved Optional Alarm and Warning Flag Bits						
112	7	Temp High Alarm	Set when internal temperature exceeds high alarm level.			
112	6	Temp Low Alarm	Set when internal temperature is below low alarm level.			
112	5	Vcc High Alarm	Set when internal supply voltage exceeds high alarm level.			
112	4	Vcc Low Alarm	Set when internal supply voltage is below low alarm level.			
112	3	TX Bias High Alarm	Set when TX Bias current exceeds high alarm level.			
112	2	TX Bias Low Alarm	Set when TX Bias current is below low alarm level.			
112	1	TX Power High Alarm	Set when TX output power exceeds high alarm level.			
112	0	TX Power Low Alarm	Set when TX output power is below low alarm level.			
113	7	RX Power High Alarm	Set when Received Power exceeds high alarm level.			
113	6	RX Power Low Alarm	Set when Received Power is below low alarm level.			
113	5	Reserved Alarm				
113	4	Reserved Alarm				
113	3	Reserved Alarm				
113	2	Reserved Alarm				
113	1	Reserved Alarm				
113	0	Reserved Alarm				
114	All	Reserved				
115	All	Reserved				
116	7	Temp High Warning	Set when internal temperature exceeds high warning level.			
116	6	Temp Low Warning	Set when internal temperature is below low warning level.			
116	5	Vcc High Warning	Set when internal supply voltage exceeds high warning level.			
116	4	Vcc Low Warning Set when internal supply voltage is below low warning lev				
116	3	TX Bias High Warning	Set when TX Bias current exceeds high warning level.			
116	2	TX Bias Low Warning	Set when TX Bias current is below low warning level.			
116	1	TX Power High Warning	Set when TX output power exceeds high warning level.			
116	0	TX Power Low Warning	Set when TX output power is below low warning level.			
117	7	RX Power High Warning	Set when Received Power exceeds high warning level.			
117	6	RX Power Low Warning	Set when Received Power is below low warning level.			
117	5	Reserved Warning				
117	4	Reserved Warning				
117	3	Reserved Warning				
117	2	Reserved Warning				
117	1	Reserved Warning				
117	0	Reserved Warning				
118	All	Reserved				
119	All	Reserved				

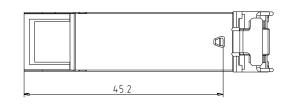


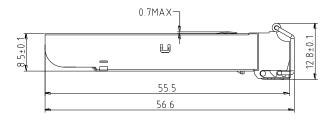
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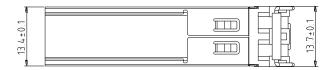
Byte	# Byte	Name	Description
120-127	8	Vendor Specific	00h.
128-255	128		Writable Memory

Mechanical Dimensions:









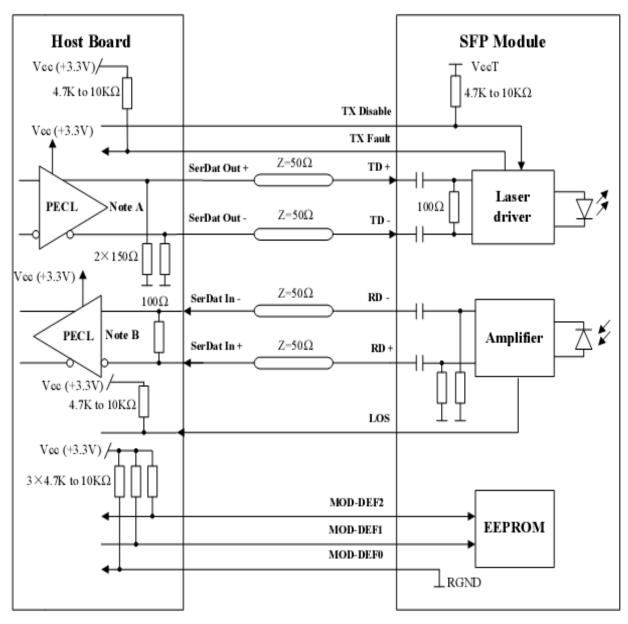


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Recommended Circuit:



Note A: Circuit assumes open emitter output

Note B: Circuit assumes high impedance internal bias @Vcc-1.3V